



				- 1
-	1545451	processor core\$1	USPAT;	2004/01/26 14:43
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1248957	TAP controller\$1	USPAT;	2004/01/26 14:43
		· ·	US-PGPUB;	
			EPO; JPO;	
İ			DERWENT;	
			IBM_TDB	
-	6006636	TAP core control logic	USPAT;	2004/01/26 14:44
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5563037	TAP control register\$!	USPAT;	2004/01/26 14:44
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	5943593	TAP control switch	USPAT;	2004/01/26 14:44
	33.3333	The Control of Strice of	US-PGPUB;	250 1,02,20 11111
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	344	non-core	USPAT;	2004/01/26 14:45
	344	Hon-core	US-PGPUB;	2007/01/20 14.43
			EPO; JPO;	
			DERWENT;	
	966075	coret1	IBM_TDB	2004/01/26 14:45
-	866075	core\$1	USPAT;	2004/01/26 14:45
!			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	1545454		IBM_TDB	2004/04/26 44:45
-	1545451	core\$1 or (processor core\$1)	USPAT;	2004/01/26 14:45
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1	0.4057	1 14 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IBM_TDB	0004/04/05 44 45
-	94957	(core\$1 or (processor core\$1)) with (TAP controller\$1)	USPAT;	2004/01/26 14:46
			US-PGPUB;	
			EPO; JPO;	
	]		DERWENT;	
	F275245	(TAD and and I lead A with (TAD	IBM_TDB	2004/04/05 14 15
-	5275310	(TAP core control logic) with (TAP control register\$!) with (TAP	USPAT;	2004/01/26 14:48
		control switch)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	
-	34	((TAP core control logic) with (TAP control register\$!) with (TAP	USPAT;	2004/01/26 14:49
		control switch)) with non-core	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3	(multi-core processor\$1) and (((core\$1 or (processor core\$1))	USPAT;	2004/01/26 14:52
		with (TAP controller\$1)) with (((TAP core control logic) with (TAP	US-PGPUB;	
		control register\$!) with (TAP control switch)) with non-core))	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	("6115763").PN.	USPAT	2004/01/26 14:52